

## Demo Board Test Report for LD7591

--- 42V/350mA LED Power Supply

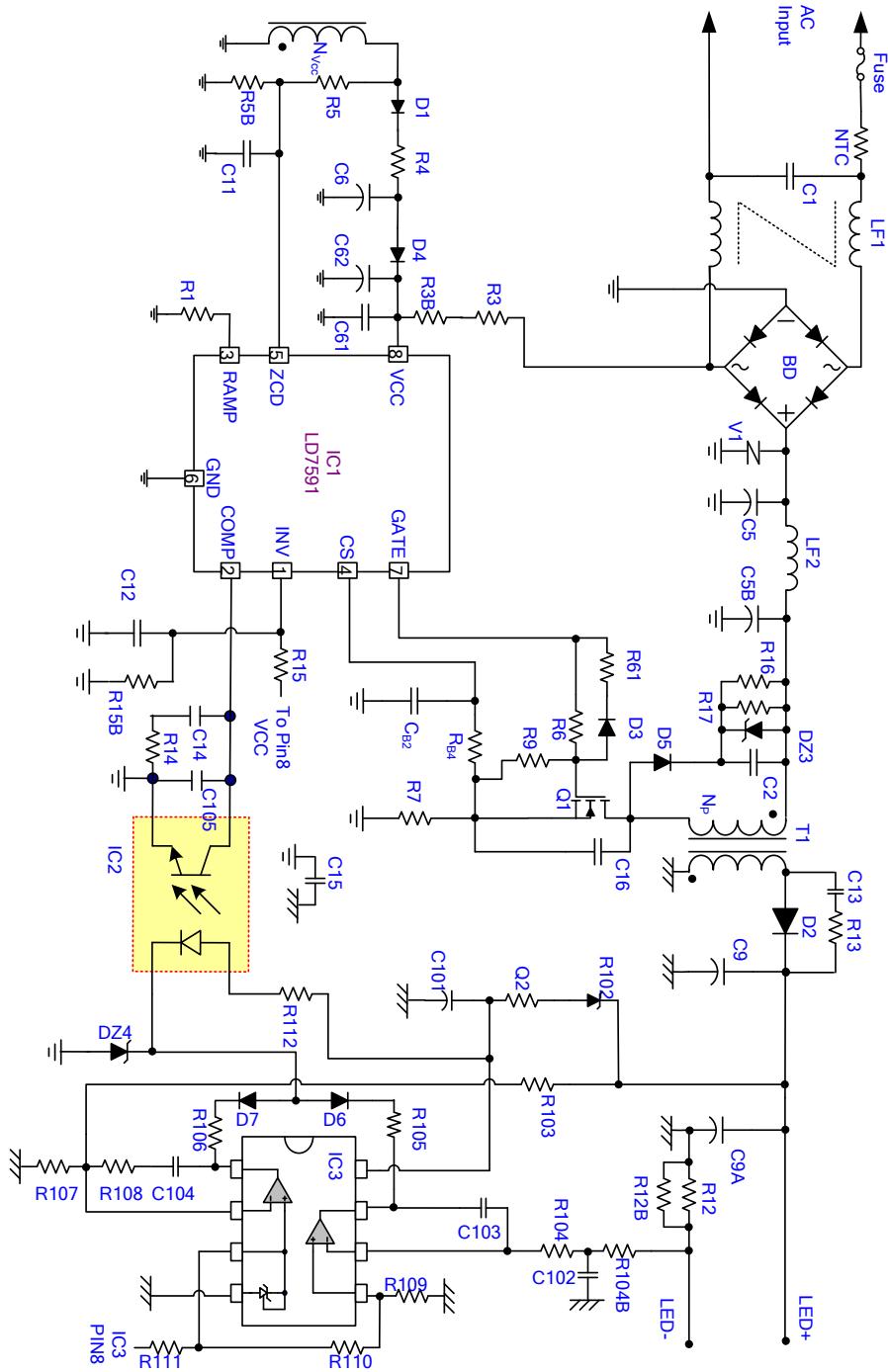
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## I. SCHEMATIC



**II. BOM**

P/N	Component Value	
Fuse	2A/250V	
NTC	0Ω, 1206	
V1	Varisitor	471
R1	27kΩ, 0805	
R2	300kΩ, 0805	
R3	110kΩ, 1206	
R3B	110kΩ, 1206	
R4	39Ω, 1206	
R5	100kΩ, 0805	
R5B	10kΩ, 0805	
R6	51Ω, 0805	
R61	0Ω, 0805	
R7	0.75Ω	1/2W
R9	20kΩ, 0805	
R12	0.68Ω,	2W
R12B	NC, 1206	
R13	100Ω, 1206	
R14	1kΩ, 0805	
R15	7.5MEGΩ, 0805	
R15B	620kΩ, 0805	
R16	100kΩ, 1206	
R17	100kΩ, 1206	
RB4	200Ω, 0805	
R101	NC, 0805	
R102	8.2V Zener	
R103	91k	
R104	0Ω, 0805	
R104B	10kΩ, 0805	
R105	20kΩ, 0805	
R106	75kΩ, 0805	
R107	5.6kΩ, 0805	
R108	39kΩ, 0805	
R109	10kΩ, 0805	
R110	100kΩ, 0805	
R111	15kΩ	
R112	4.7kΩ, 0805	
R113	NC, 0805	
R114	0Ω, 0805	
IC1	LD7591	SOP-8
IC2	PC817	
IC3	TSM103, SOP8	

P/N	Component Value	Note
C1	0.1μF / 275VAC	X-cap
C2	4.7nF/1kV,1206	
C5	0.047μF / 400V	MPF 塑膠電容
C5B	0.1μF / 400V	MPF 塑膠電容
C6	22uF/ 50V	Electrolytic Capacitor
C61	104pF/25V/0805	
C62	33uF/ 50V	
C9	330μF, 50V	Electrolytic Capacitor
C9A	330μF, 50V	Electrolytic Capacitor
C11	NC, 0805	
C12	10pF, 0805	
C13	470pF/500V, 1206	
C14	0.47μF / 16V, 0805	
C15	2200pF,	Y 電容
C16	NC /1kV,1206	
C101	2.2uF/ 50V	
C102	4.7μF/10V/0805	
C103	0.1μF/ 25V, 0805	
C104	1μF/ 25V, 0805	
C105	473pF/25V/0805	
CB2	220pF/16V, 0805	
D1	BAV103	
D2	ER502	200V/ 5A,
D3	LL4148	SOD-80
D4	LL4148	SOD-80
D5	1N4007	1000V/1A
D6	LL4148	SOD-80
D7	LL4148	SOD-80
DZ1	NC	
DZ2	NC	
DZ4	36V Zener	
DZ3	P6KE200A	DO-15
BD	DI106	600V/1A
T1	EF20, 1150uH	106/32/13
LF1	UU9.8	
LF2	1000uH	
Q1	FQPF5N60C	600V, 4.5A, TO-220
Q2	330R, 0805	

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### III. EXECUTIVE SUMMARY

Office	Taipei
Model Name	LD7591-DemoBoard#01
Version	01
IC	LD7591(D/C:)

TEST	Result	Comments
3. Load Regulation	PASS	
4. Turn On Delay Time	PASS	
5. Efficiency , PF Test	PASS	
6. Stress Voltage Test	PASS	
7. Thermal Test	PASS	

## 1. Input Voltage & Frequency

The unit shall be capable of operating as a universal AC input power supply accepting AC inputs. The power supply shall operate between the following voltages (from 90V to 264V). The supply will be designed to operate for a Table 1.

	Minimum	Normal	Maximum
Input Voltage	90Vac	110Vac	264Vac
Frequency	47HZ	60HZ	63HZ

Table 1.

## 2. Output Loads

The line and load regulation for each of the outputs are shown in Table. 2.

Parameter	Output Voltage			Output Current	
	Minimum	Typical	Maximum	Minimum	Maximum
+42V		42V		0A	0.35A
Line Regulation	-5%	/	+5%	/	0.35A
Load Regulation	-5%	/	+5%	0A	0.35A

Table 2.

### 3. Load Regulation

**Test Conditions:****Input: 90Vac/115Vac/230Vac/264Vac(60Hz)****Output:** Electronic Load Setup:

CV mode (no load、31.5V、35.0V、38.5V、42.0V、42.5V、43.0V)

CC mode( $I_o=0.05A$ 、 $0.15A$ 、 $0.25A$ )**Ambient Temperature :  $25^{\circ}\text{C}$** 

CV mode:

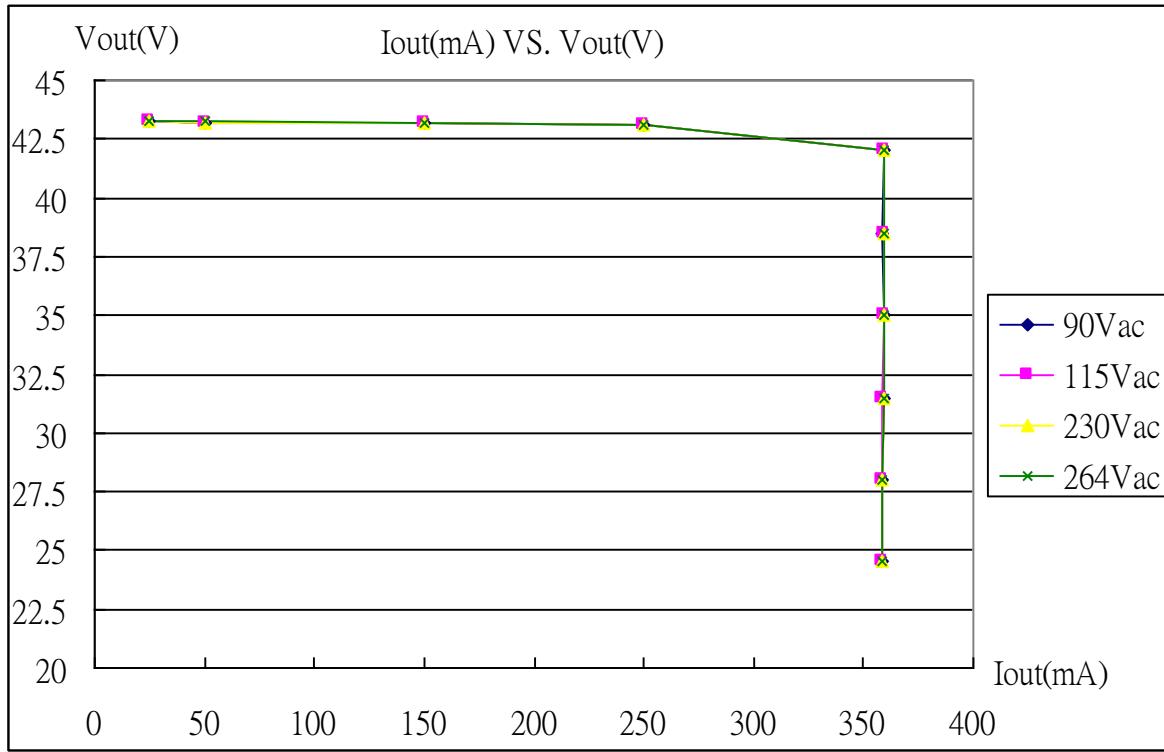
$V_{in}(\text{V}_{\text{AC}})$	90Vac					
$V_{out}(\text{V}_{\text{DC}})$	24.5	28	31.5	35	38.5	42
$I_{out}(\text{mA})$	358.8	358.8	359	359	358.9	359
<hr/>						
$V_{in}(\text{V}_{\text{AC}})$	115Vac					
$V_{out}(\text{V}_{\text{DC}})$	24.5	28	31.5	35	38.5	42
$I_{out}(\text{mA})$	358.9	358.8	358.9	359	359	359.1
<hr/>						
$V_{in}(\text{V}_{\text{AC}})$	230Vac					
$V_{out}(\text{V}_{\text{DC}})$	24.5	28	31.5	35	38.5	42
$I_{out}(\text{mA})$	358.9	358.8	359	359	359	359.1
<hr/>						
$V_{in}(\text{V}_{\text{AC}})$	264Vac					
$V_{out}(\text{V}_{\text{DC}})$	24.5	28	31.5	35	38.5	42
$I_{out}(\text{mA})$	358.9	358.9	359	359	359.1	359

Table 3-1

CC mode:

$V_{in}(V_{AC})$	90Vac			
$I_{out}(A)$	0.025	0.05	0.15	0.25
$V_{out}(V_{DC})$	43.25	43.22	43.16	43.09
<hr/>				
$V_{in}(V_{AC})$	115Vac			
$I_{out}(A)$	0.025	0.05	0.15	0.25
$V_{out}(V_{DC})$	43.25	43.22	43.17	43.09
<hr/>				
$V_{in}(V_{AC})$	230Vac			
$I_{out}(A)$	0.025	0.05	0.15	0.25
$V_{out}(V_{DC})$	43.25	43.22	43.17	43.09
<hr/>				
$V_{in}(V_{AC})$	264Vac			
$I_{out}(A)$	0.025	0.05	0.15	0.25
$V_{out}(V_{DC})$	43.25	43.23	43.17	43.09

Table 3-2



#### 4. Turn On Delay Time

Turn on delay time will be less than 3 seconds at full load. Turn on delay time is measured as the delay between input voltage being applied at 0° phase angle and when the outputs arrive within 10% of their operating value. Turn on delay time is measured using an input voltage of 90VAC(rms) and input frequency of 60Hz.

##### Test Conditions:

**Input: 90Vac(60Hz)**

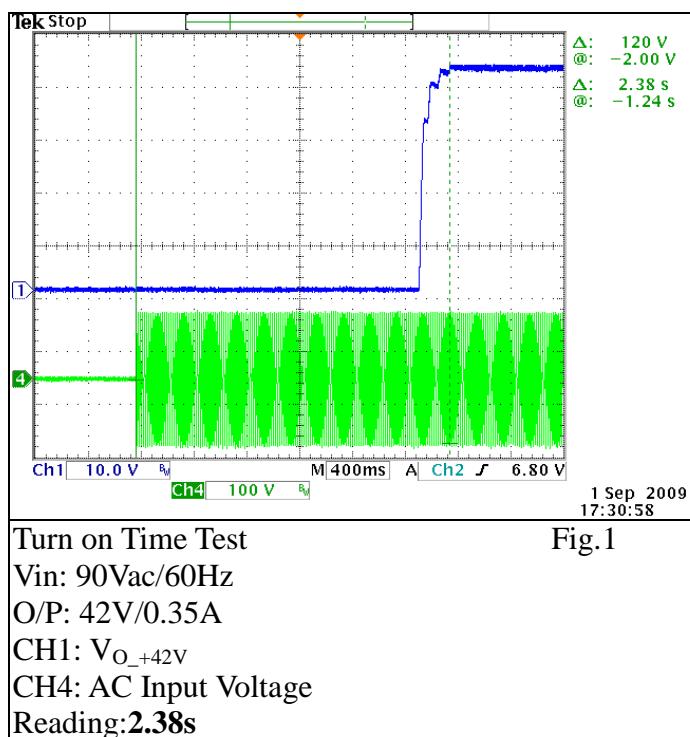
**Output:** 42V/0.35A (Electronic Load Setup:CV mode)

**Ambient Temperature :** 25°C

**Test Result:** PASS

Input	T <sub>turn on delay</sub>
<b>90Vac</b>	2.38s

Table 4.



**5. Efficiency and PF Test**

The efficiency of power supply shall be measured throughout its specified operating input range and at output maximum load conditions. It should remain **80% minimum**. PF > 0.85 .

**Test Condition:****Input: 90Vac/115Vac/230Vac/264Vac(60Hz)****Output:** 42V/0.35A (Electronic Load Setup:CV mode)**Ambient Temperature:** 25°C

	<b>90V</b>	<b>115V</b>	<b>230V</b>	<b>264V</b>
<b>PF</b>	<b>0.9793</b>	<b>0.9909</b>	<b>0.9447</b>	<b>0.9205</b>
<b>Efficiency</b>	83.93%	85.29%	85.50%	84.59%

Table 5 Efficiency, PF TEST.

## 6. Power Component Stress Voltage

### Test Condition:

- Set the output loads at full load and ambient 25 °C.
- The PSU test on everyone voltage and frequency.

### Check:

- Under Steady state the derating shall be below **100%**.
- Under Transient state the derating shall be below **100%**.
- Input line bulk capacitors limits are **100%** (continuous).

### Result:

**Input Voltage:** 90Vac/264Vac (47/63Hz)

**Output Power:** 42V/0.35A (Electronic Load Setup:CV mode)

No.	Location	Max. Rating(V)	Steady State(90V / 47HZ)	
			Measurement	Derating(%)
			V	V
1	Q1	600	266	44.33%
2	D2	200	83.6	41.80%
3	D1	100	33.8	33.80%
4	D5	600	280	46.67%

Table 6-1.

No.	Location	Max. Rating(V)	Steady State(264V / 63HZ)	
			Measurement	Derating(%)
			V	V
1	Q1	600	508	84.67%
2	D2	200	153	76.50%
3	D1	100	62.4	62.40%
4	D5	600	284	47.33%

Table 6-2.

No.	Location	Max. Rating(V)	Transient State(90V / 47HZ)	
			Measurement	Derating(%)
			V	V
1	Q1	600	270	45.00%
2	D2	200	85.6	42.80%
3	D1	100	35.6	35.60%
4	D5	600	508	84.67%

Table 7-1.

No.	Location	Max. Rating(V)	Transient State(264V / 63HZ)	
			Measurement	Derating(%)
			V	V
1	Q1	600	540	90.00%
2	D2	200	187	93.50%
3	D1	100	72	72.00%
4	D5	600	530	88.33%

Table 7-2.

## 7. Thermal Test

### Test Condition:

- Set the output loads at full load and ambient **25°C**.
- The PSU test on everyone voltage and frequency.
- Burn-In 2 hours

### Check:

- All of component and magnetic device (transformer, Filter choke) shall NOT exceed 100°C.

### Result:

No.	Location	Max. Rating(°C.)	90V/47Hz(°C.)	264/63Hz(°C.)	Derating(%)	
					90V/47Hz	264/63Hz
1	BD	150	54.2	41.6	36.13%	27.73%
2	DZ3	150	69.7	72.5	46.47%	48.33%
3	D5	150	72.5	74.2	48.33%	49.47%
4	T1	130	68.5	71.7	52.69%	55.15%
5	D2	150	58.5	69.9	39.00%	46.60%
6	R12	150	42.6	48	28.40%	32.00%
7	Q1 Body	150	62.2	63.9	41.47%	42.60%
8	IC1	150	42.5	44.5	28.33%	29.67%
9	R16	150	74.2	72	49.47%	48.00%
10	R16	150	75.1	77.1	50.07%	51.40%
11	C2	105	70	70.8	66.67%	67.43%
Ambient				--	--	--

Table 8. Key Parts for Thermal Test

8. EMI

#### Test Condition:

The power supply should comply with FCC part15,EN 55022 and CISPR22 meeting Class B for conducted emissions with a 3dB margin. Tested unit should be connected to a pure resistor load (rated loading). The test condition shall be followed as:110 VAC(L and N),220VAC(L and N)

Test Result: PASS

**Vin=110Vac、Line、Vo=42V、Io=0.35A ( 14.7W )**



**Leadtrend Technology Corp.**

### Site C

Customer Name: system test

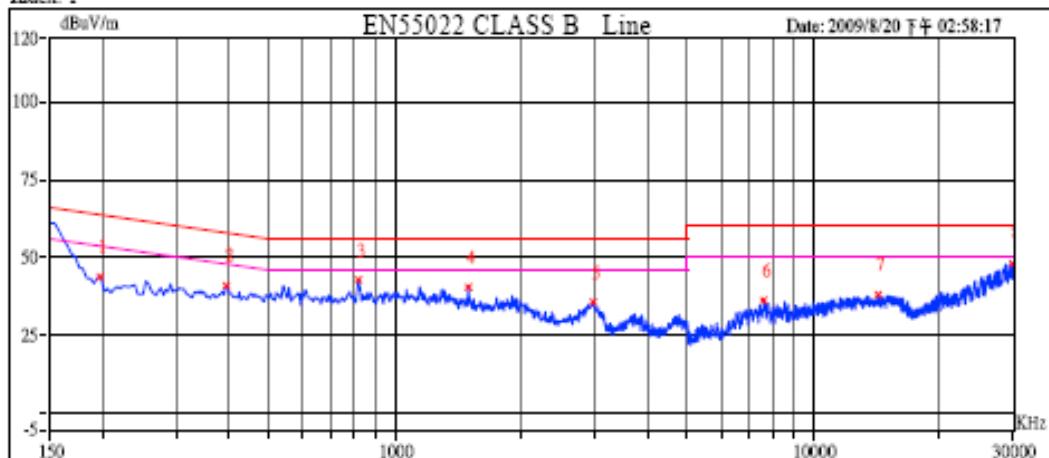
Model Name: JD7501

Model Name:  
Test Mode:

Project No.: 110V Line

Project Name: HCL  
Engineer Name: jolin

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**Vin=110Vac、Neutral、Vo=42V、Io=0.35A ( 14.7W )**



**Leadtrend Technology Corp.**

通嘉科技股份有限公司

Customer Name: system test

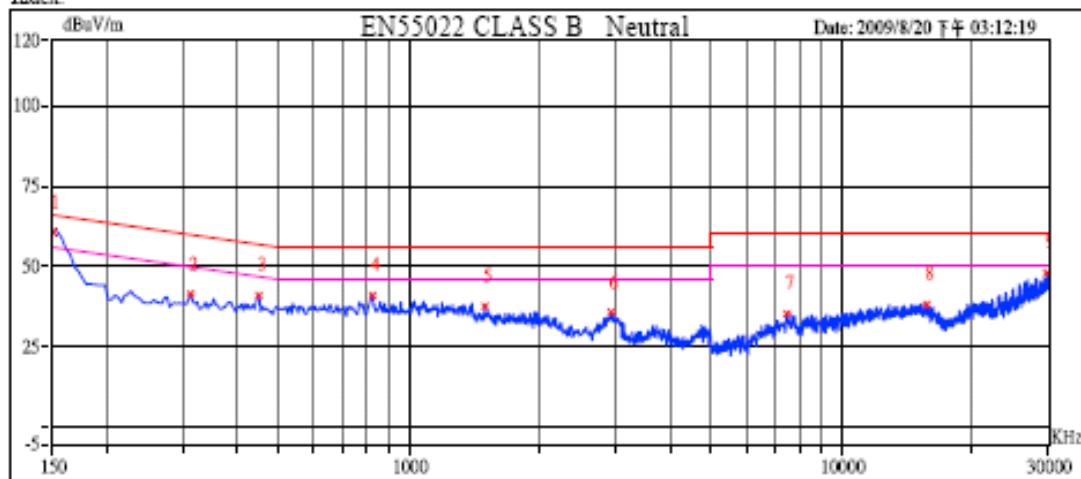
Model Name: LD7591

Test Mode:

Project No.: 110V\_Nature

Engineer Name: jolin

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**Vin=220Vac、Line、Vo=42V、Io=0.35A ( 14.7W )**



**Leadtrend Technology Corp.**  
通嘉科技股份有限公司

### **Site C**

Customer Name: system test

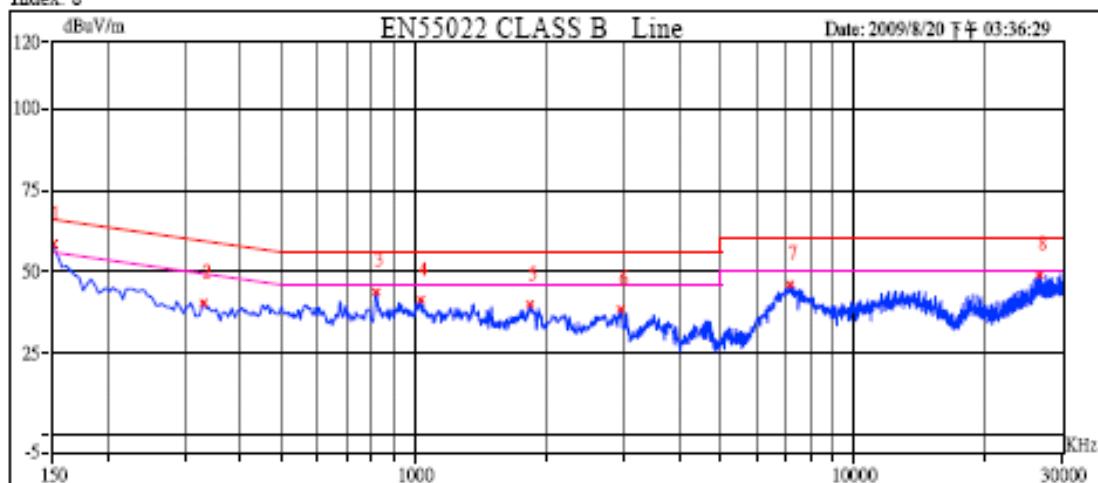
Model Name: LD7591

Model Test

Project No.: 220V Line

Engineer Name: jolin

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**Vin=220Vac、Neutral、Vo=42V、Io=0.35A ( 14.7W )**



**Leadtrend Technology Corp.**

### Site C

Customer Name: system test

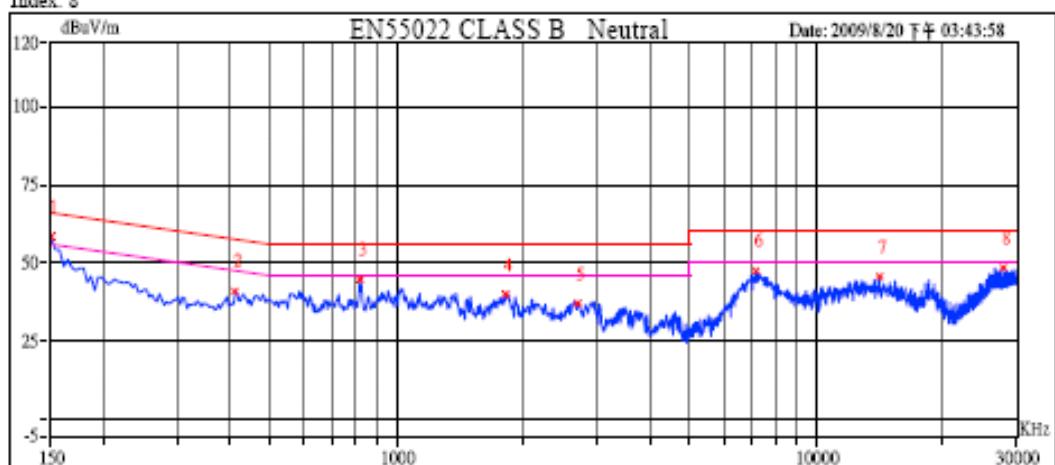
Project No.: 220V\_Nature

Model Name: LD7591

Engineer Name: jolin

Model Name:  
Test Model

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## 9. Surge For System

### Test Condition:

High Energy Transients are applied to the power supply once each 20 second period with 5 transients per test. The surge Test defines four levels of peak voltage.

### Check:

Survival: No component shall be damage electrically during the tests. The PSU shall continue to operate in a safe manner during abnormal operation.

### Result:

**Input Voltage:** 220V (60Hz)

**Output Power:** Max Load

Surge voltage	Coupling Mode	Test Level	Phase		Repetition	Test Result
1KV	Diff.	$\pm 1\text{KV}$	0	L to N	5 pulses 20Sec	Pass
			90			Pass
			180			Pass
			270			Pass

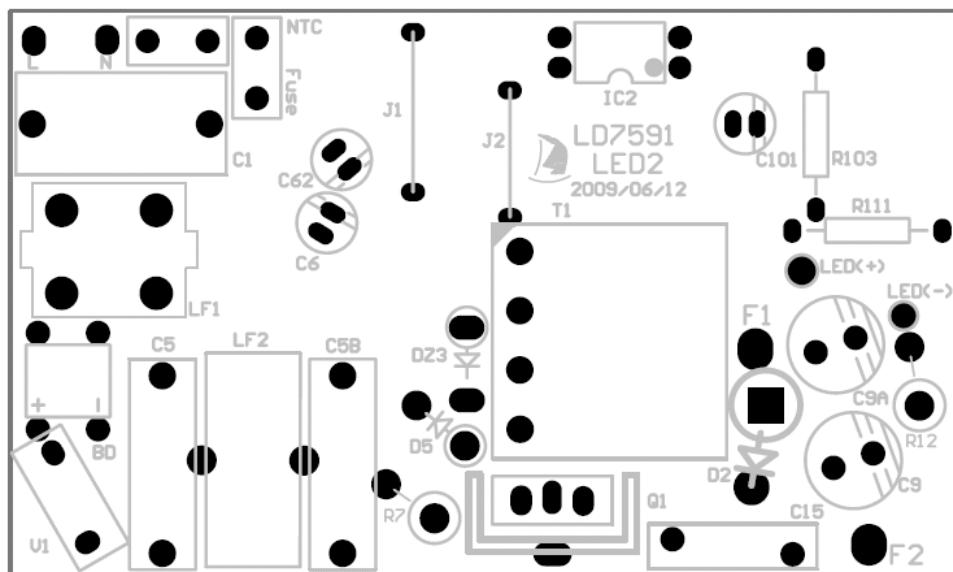
Table 9

Surge voltage	Coupling Mode	Test Level	Phase		Repetition	Test Result
2KV	COM..	$\pm 2\text{KV}$	0	L to Earth GND N to Earth GND	5 pulses 20Sec	Pass
			90			Pass
			180			Pass
			270			Pass

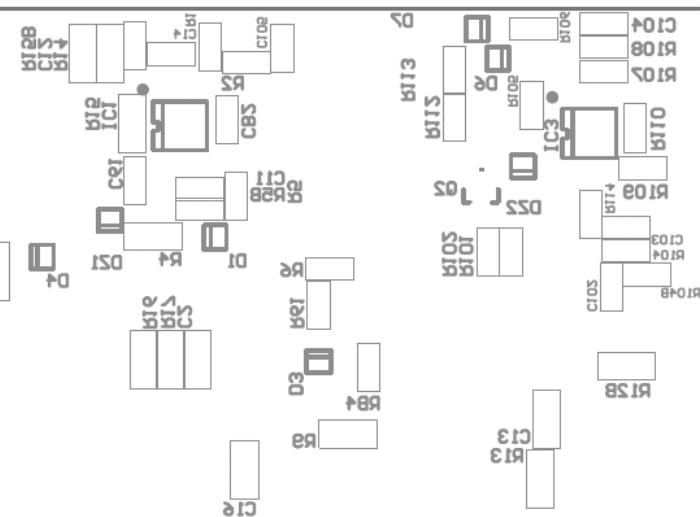
Table 10

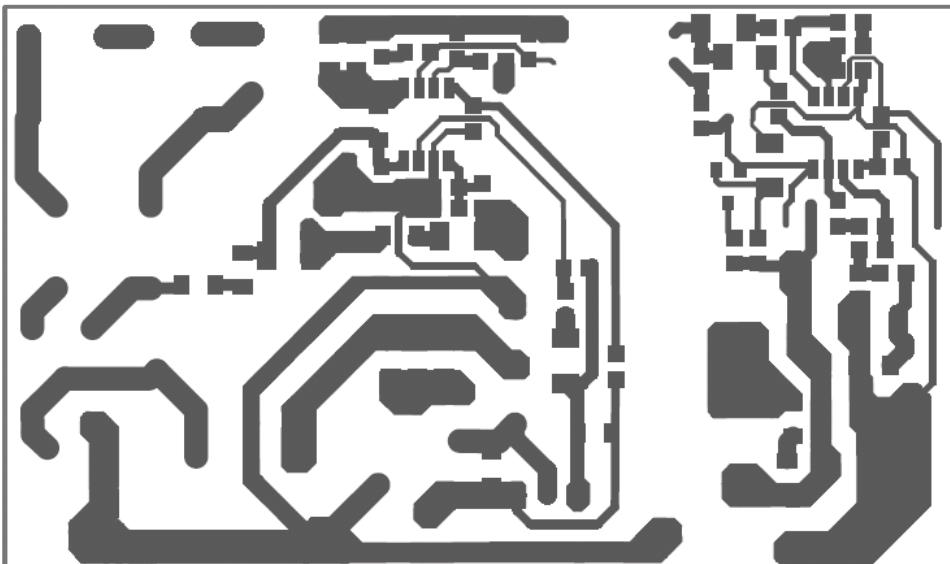
#### **IV. Gerber File:**

## Silkscreen TOP



## Silkscreen Bottom



**Bottom Layer****Soldermask Bottom**